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K&D

Declass Review by NIMA/DOD

30 March 1965

Ref: LJC 139-1445



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Contracting Officer

Subject: Contract No. 825

Gentlemen:

As you requested in your letter dated 18 February 1965, we have investigated the following areas:

Item A - An optional conversion of the chip printer to color printing from color input film. *Punching*

Item B - Conversion of the chip printer's auxiliary printing system from the proposed FONT-Hammer System to a silicon diode array data block. *250K*

Item C - A change in the proposed location of the auxiliary information (across the narrow end of the film chip) to placement along the long edge of the film chip with security classification printed on the narrow ends. *No Cost.*

Item D - Development of a method of roll film tension control that obviates the use of sensor arms that could cause film damage. *No Cost*

Item E - Development of a method of film metering (applicable to the chip printer) that does not require the conventional rubber faced metering roller which, on some transport systems, faces the emulsion side of the film and causes film damage. *No Cost*

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Items A and B, as listed above, are not included in the present Proposal. Item A is being held due to a lack of information on new high resolution color film such as example; can it be produced in chip form without severe curl, can it be processed and how, what are the full illumination requirements. The difficulties encountered here have been expressed to your technical representative in a separate letter dated 22 March 1965. In order to resolve those questions, as stated in the above letter, it is requested that you arrange a meeting between you, ourselves, and Eastman Kodak--if possible, within the next two weeks--so that we may be able to establish the basic design parameters which will ensure that all operational functions are capable of good color reproduction based upon the latest technology.

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Insofar as Item B is concerned, a technical investigation in the area of actual production items and associated pricing is now underway between and our Semiconductor Division. It is expected at this time that formal presentation of this item will be made on or before 15 April 1965.

We have investigated Items C, D, and E, and our approach to the solution of these areas is outlined in Technical Proposal No. SME-105-64-40, dated 26 March 1965, four (4) copies of which are enclosed.

We wish to advise you that Items C, D, and E, as recommended in our Technical Proposal, can be incorporated into the subject contract at no additional cost, providing they are contractually authorized on or before the start of Phase II.

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Contracts

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